Introduction and Background

Considering that cars have been developed and sold commercially since the end of the 19th century, high-speed sensors and displays are a comparably recent event. At the end of the 20th century, more than 100 years after the start of commercial car sales, high-speed sensors and displays were, if at all, presented in concept cars or sold with selected luxury models. However, since the turn of the 21st century, the number of sensors and displays has grown, with the market really just gaining momentum at the time of writing in 2021. While the exact number for the expected market growth differ, market research agrees on the trend: it is significant. In [1], for example, the number of cameras per car is expected to grow between 2020 and 2030 from five to 20 and the number of displays from three to 15.

Displays and cameras are thereby not only growing in numbers, they are also growing in resolutions. Furthermore, thanks to the increasing adoption of Advanced Driver ASsist (ADAS) functions, the number of sensors other than cameras is also growing, as is the number of types of sensors. The race for being the first to successfully achieve the ultimate ADAS function where driver intervention is no longer required – level 4 or 5 Autonomous Driving (AD) [2] – is accelerating the trend in two different ways. First of all, more sensors are deployed in order to reduce the number of tasks drivers have to perform. Then, the drivers can use that freed capacity in order to focus more on information and entertainment (infotainment) on the displays provided.

All these innovations are spurred by key technological inventions and developments. Next to the continuing empowerment and shrinking of digital processing technologies that are responsible for many amenities of modern life in general, more specific inventions are: high-resolution digital image sensor technologies, empowering (new types of) sensors for automotive use like Light Detection And Ranging (Lidar) sensors, digital video (compression) formats, digital display technologies that are small, robust, and cost efficient enough to be commonly used inside cars, and modern user interaction methodologies proliferated by the use of smartphones (plus the mobile communication telecom infrastructure enabling it).

One of the resulting key challenges for deploying all the sensors and displays inside cars is how to integrate them into the Electric and Electronic (EE-)architectures and, especially, how to realize their communication. When the adoption of (digital) cameras and displays in cars started at the beginning of the 21st century, the actual communication was analogue. However, analogue video transmission has severe limits with respect to resolution and quality, which prohibits the subsequent processing necessary to realize modern ADAS and infotainment functions. So nowadays, digital video data transmission drives the demand for data rates in the In-Vehicle Communication (IVC) systems, while the availability of suitable high-speed communication technologies opens the door for innovations with respect to video-related customer functions and EE-architecture choices.

Unfortunately, it is thereby generally not possible to simply reuse the communication technologies from the consumer and IT industries, which already support the required high video data rates in a mass market. It is one goal of this book to explain the additional constraints IVC technologies have to master with respect to robustness and costs, why automotive suitable physical layer developments are important, and why Automotive SerDes and Automotive Ethernet technologies are the available choices in this context. In order to support a profound understanding of the interrelations between the automotive environment, the high-speed sensor and display use cases, and the communication technologies, and to motivate the choices, this book is structured as follows:

- In the continuation of this introductory Chapter 1, Section 1.1 motivates the focus on sensor and display applications. It explains the differences between sensor and display applications and between them and other use cases inside cars. Section 1.2 introduces the terminology used in the context of SerDes communication and the background of Automotive SerDes. Section 1.3 provides information on the origin of Ethernet as such and on Ethernet used as an IVC technology.
- Innovations and their underlying technologies are rarely introduced for the sake of "using a new technology". Normally, they serve a purpose. The three main reasons for innovations in the industrial Business-to-Business (B2B) environment are: first, to allow for new functionalities (and business), second, to save costs, and/or third, to fulfill new regulatory requirements. In order to provide the context, this book introduces first, in Chapter 2, the high-speed sensor and display use cases with respect to their history in the car industry as well as the underlying technical and architectural choices in more detail.
- Chapter 3 introduces the automotive environment, in which the use cases have to function reliably and safely. Cars are particularly complex products, because they have to provide a vast variety of functions under extremely different conditions, while needing to be attractive to customers in a very competitive market. The automotive environment impacts all technical choices made for cars and is therefore covered early in this book.
- One reason consumer and IT communication technologies are often not usable in cars, is their incapability to meet the automotive ElectroMagnetic Compatibility (EMC) requirements (at least not at reasonable costs). EMC is especially important for all electronics inside cars, and thus detailed in a separate Chapter 4.
- The cable harness is the third heaviest and third most expensive component inside cars [3]. Communication cables need to be robust, cost efficient, and light at the same time. One more reason why consumer grade products are generally unsuitable for in-vehicle use. Chapter 5 introduces general choices for the communication channel that have to be made for all IVC technologies. This includes options for cables and connectors.
- Power supply and power saving is another extremely important aspect in cars, independent of the actual technologies used. Aspects relevant for sensor and display use cases that impact the IVC technology in general are discussed in Chapter 6.
- Chapter 7 introduces the choices for Automotive SerDes technologies.

- Chapter 8 introduces the High-Speed (HS) Automotive Ethernet technologies and provides a general comparison between HS Automotive Ethernet and SerDes standards.
- Both, Automotive SerDes and Automotive Ethernet are first of all use case independent physical and data link layer technologies. To deploy them for high-speed sensor and display use cases, quite a number of related higher layer standards and protocols are added, which might also affect or become part of specific SerDes or Ethernet products. Chapter 9 provides and overview and introduction to many related standards and protocols. These comprise color codes, control interfaces, video compression formats, content protection, as well as camera and display specific protocols.
- Last but not least, Chapter 10 looks at test, qualification, and tools. That they can be tested and serviced is an extremely important aspect for all use cases and technical solutions in cars. So, while this topic is addressed at the end of this books, to ensure testability for all system designs and new technologies is actually an important starting requirement.

Note that, while the order of content and chapters is intended to be as logical and sequential as possible, a perfect order does not exist for a subject as complex as the one addressed in this book. There are many interrelations between chapters, so that the book contains many forward and backward references.

1.1 The Distinctive Properties of High-Speed Sensor and Display Use Cases

Displays and sensors in cars – including cameras as a special type of sensor – actually address quite distinct use cases. Displays have the sole purpose of relaying technical, entertainment, or other information to the car users. Especially when backed by touch functionality, voice recognition, or related dials and knobs, they serve as an important element of the Human Machine Interface (HMI), with which the customers can control various functions inside their cars.

Sensors on the other hand, provide sensor specific, technical data that is, in its raw format, generally unusable to car occupants. Either the sensor data serves directly to control driving functions without the users ever being aware of their existence or it needs to be processed before it can be used for driver or passenger information or user interaction in ADAS functions. Camera images are the exception, as they might be used for machine vision/ processing as well as for human vision, for example in back-up camera systems.

Table 1.1 lists additional properties that differ for display, camera, and other sensor use cases and that have some relevance for the architecture and other technical choices of the use cases discussed in the following chapters of this book, especially in Chapter 2. Table 1.1 also motivates why it makes sense to address cameras separately from other sensors. While there are some similarities between cameras and other sensors, there are also important differences.

	Displays	Cameras	Other sensors
Data recipient	Human vision only	Human vision or machine processing	Machine preprocessing required
Quality of Service (QoS) requirements	Human vision allows for some latency and losses	Machine processing requires low latencies and is sensitive to losses, compression or other	
Size of housing	Generally large	Typically very small	
Power requirement	Power hungry because of the display (depends on size)	Small housings easily accumulate heat, which can impede the sensing quality. Power dissipation should therefore be low	
Location in car	Inside the cabin with stringent location require- ments with respect to the occupants' positions	Facing outside or to the driver or other occupants	ADAS sensors are typically on the body shell facing outside, other sensors might be anywhere includ- ing under the hood
Possible add-on functions	Might comprise micro- phones, Consumer Elec- tronics (CE) connectivity (including auxiliary sock- ets), or even cameras, typ- ically no speakers though	Might comprise InfraRed (IR) Light Emitting Diodes (LEDs) for interior cameras and night vision, exterior cameras might comprise heating	Generally singular, collects one type of data only

Table 1.1 Comparison of distinct sensor and display use case properties

There are, however, also aspects that unite the use cases. These are their requirement for highly asymmetric (high-speed) data communication and the related architectural choices. Both, sensor and display units, are generally located at the edge of a network as end nodes. Even if they are forwarding data in a type of display or sensor daisy chain – which happens seldom in any case – they can be designed such that they require no software-based processing, which might require frequent updates otherwise. These aspects not only unite the high-speed sensor and display use cases, it distinguishes them from (many) other Electronic Control Units (ECUs) inside the car.

Figure 1.1 shows two, fundamentally different architecture options. In order to directly compare the sensor and display use cases, the examples depicted assume that the sensor data is – after having been processed accordingly – displayed on a screen to the user. In a real car, such direct link between one sensor and display is seldom. A display might also be used to present pre-stored entertainment data, or they show aggregated results from the evaluation of various sensors. Sensors outputs, on the other hand, might result in vehicle control without user interaction or with audible feedback only.

The upper part of Figure 1.1 depicts the case in which sensor and display contain no video or sensor data processing themselves. The sensor data is transferred as collected (more or less) to the ECU, which processes the data, makes use of the result in its application, and then renders this into a video stream that is transferred to the display where it is presented on a screen. This could be the setup for a back-up camera. Colloquially, this scenario is often referred to as having "dumb" sensors and displays. The sensors and displays have no processing and thus "no intelligence". While some might object to the exact wording, key is that the sensors and displays in this scenario do not run any software that might require regular updates or upgrades.



Figure 1.1 Principle architecture options for sensor and display use cases

In the lower part of Figure 1.1 the sensor as well as the display perform the major processing themselves. A typical example would be traffic sign recognition. The camera records the image, identifies the particular traffic sign in its processor, and then transfers only an identifier number to the ECU. The ECU would then perform a plausibility check in its application processing by comparing the identified traffic sign with its map data, before sending itself an identifier number to the display. The display then renders a picture of the sign that is displayed to the customer. Naturally, such a scenario makes the sensors and displays more complex. However, at the same time the amount of data that needs to be communicated is significantly smaller than in the case of sensors and displays without processing. The additional costs for the processing is potentially compensated for with a less expensive communication system that does no longer need to be "high-speed".

Note that in some cases the only processing that is being performed in the sensors and displays is data compression or decompression. This somewhat intermediate case is not depicted in Figure 1.1. The extra processing needed in the sensors and displays can often be realized in hardware. In general, hardware compression is faster and less power consuming than compression in software. With compression the data rate is decreased, but not as much as when just identifiers are transmitted, which would be the case after full processing. So, a scenario with compression would result in intermediate processing and intermediate data rate. At the same time, the compression might have other impacts, such as compression losses or added processing latencies, which might not be acceptable (see also Table 1.1). For more details on the use cases, see Chapter 2.

What is important in the context of this book: In both scenarios depicted in Figure 1.1, it is necessary to distinguish between the protocol interfaces that are used within the sensors or displays and the IVC technology. The protocol interfaces used for connecting the sensor and display chips are application specific, meaning that the imager interface technology inside a camera cannot be used for putting data onto the screen of a display and vice versa. At the same time, both camera and display might be connected to the ECU using the same IVC technology. Furthermore, the IVC chips used in both cases are not necessarily the same. This is why Figure 1.1, distinguishes between IVC chips with and without " '". In the upper part of Figure 1.1, it is likely necessary to use an "IVC bridge" that bridges between the

use case agnostic IVC technology and the use case specific protocol. In the lower part of Figure 1.1, the interface combination used, it depends on the availability of interfaces in the processing and IVC.

1.2 Background to Automotive SerDes

The term "SerDes" is used for a number of different technologies in different use cases and scenarios. This section aims to clarify the ambiguity of the term at least for the use within this book. In order to do so, Section 1.2.1 starts with explaining the origin of the term "SerDes". Section 1.2.2 introduces the SerDes terminology common in the automotive industry and Section 1.2.3 outlines the status of Automotive SerDes in the car industry. The technical choices and properties of the Automotive SerDes technologies as such are discussed in Chapter 7.

1.2.1 The Origin of "SerDes"

"SerDes" first of all describes a very basic physical principle. When two chips had to communicate in the early days, each output pin of one chip was simply directly connected to the input pins of the other chip and vice versa. When more than one information had to be exchanged, other sets of parallel pins and connections were added. For reasons explained in more detail further below, having more parallel data lines became impractical, and formerly parallel data was serialized before being transferred to other chips. There, it would be deserialized before being processed internally. Figure 1.2 shows this in a very simple example. To have this serializer-deserializer conversion of data at both ends of the communication then condensed into the term "SerDes".



Figure 1.2 The basic principle of SERializer-DESerializer (SerDes) technologies

There are three main reasons to favor serial data transfer over parallel transmission [4] [5]:

- 1. lower number of pins at the Integrated Circuits (ICs)
- 2. better synchronization and supported data rates
- 3. less interference, especially less crosstalk

Ad 1. Lower number of pins at the Integrated Circuits (ICs)

Since their invention, the processing capabilities of IC's made huge progress. Moore's law observed that the transistor density has about doubled every two years [6]. At the same time, the packaging and pin density of ICs has not developed at the same pace, meaning that continued parallel data transmission would have resulted in prohibitively large ICs. This simply mandated using the existing pins more efficiently.

Ad 2. Better synchronization and supported data rates

Figure 1.3 shows a simple parallel transmission system consisting of one transmitter (TX), one receiver (RX), eight parallel data lines (D0 to D7), and one clock line (CLK). The clock line is important, because for the receiving unit it is essential that all eight lines are synchronized in order to be able to process the received data correctly. To the right of the TX – RX system shown in Figure 1.3, an example bit pattern is depicted as seen by the receiver. The upper part of Figure 1.3 shows the ideal situation. Here, the data of each data line is received in perfect synchronization. This might well be the case for low frequencies and short distances on well-designed Printed Circuit Board (PCB) layouts. The lower part of Figure 1.3 depicts – in a strongly simplified way – what can happen if the parallel data paths are not perfectly aligned. In this case, the receiver might not sample all bits in the same transmit slot.



Figure 1.3 Synchronization issue in case of parallel data transmission

In this simplified figure, the data paths have unequal lengths. In real life such variations also depend on the chip process, voltage, and/or temperature. The higher the frequency, the more sensitive the system is to such delay variations, with the result that from a certain frequency on, it is not possible to reliably receive data transmitted on parallel lanes.

Naturally, transmitting over long cables increases the difficulties when compared with the transmission on a PCB.

A serial system does not have such synchronization problems, even if it needs to transmit with an n-times higher data rate in order to achieve the same throughput, when compared with a transmission over n parallel lanes.

Ad 3. Less interference, especially less crosstalk

Another important aspect in parallel data communication is the reference potential of the signals, the signal ground. The parallel data transmission as depicted in Figure 1.3 is single-ended and not differential. Single-ended means that one lane or wire carries the varying voltage levels that represents the signal while the other lane or wire needed for the communication is, usually, the ground.

Such a communication concept is quite susceptible to interference and would require a perfect signal ground to mitigate the effects of, for example, crosstalk. Crosstalk is the interference between adjacent data lines. The longer and closer the lines or cables and the higher the transmit frequency, the more severe the impact of crosstalk. In case of parallel data transmission, there are many adjacent lines per definition and the risk of crosstalk impairments is therefore high. To mitigate the impact of crosstalk, ground lines could be put between all parallel data lines on a PCB, meaning that at least the same number of signal ground lines are connected between the transmitter and the receiver.

Serialized data allows easily for differential transmission. In case of differential transmission the same signal is transmitted over two wires with opposite voltage levels. At the receiver of a differentially transmitted signal, the two signals are combined. This cancels out various noise sources. Serialized data with differential transmission thus has better interference robustness and avoids the impact of the signal ground on the signal integrity.

High-speed SerDes has thus become the dominant form of input and output for (most) high-integration chips [4] and almost all modern communication technologies are based on the serialization/deserialization principle shown in Figure 1.2. The simple example of Figure 1.2 is single ended, it uses a dedicated clock line, and a dedicated voltage level for a single signal. Modern SerDes technologies are differential and do not need a dedicated clock line. The enhanced circuit technology can recover a stable and precise clock signal from the bit stream received. This further improves the robustness of the SerDes technologies as differences in transmission time between the clock and the data signal ("clock skew") are eliminated. Furthermore, the available circuit technologies allow modulating and encoding the transmitted data prior to sending it. This means that with a single, physical voltage level, more than one bit can be transferred, and the data rate can be increased (see also Chapter 7 for more details on actual solutions).

1.2.2 Automotive SerDes Terminology

The previous Section 1.2.1 explained, why the term "SerDes" might be used in different contexts for quite different communication technologies. "SerDes" as a physical principle does not distinguish whether the communication is on a Printed Circuit Board (PCB), across a wire, or even wireless. Often, even Ethernet is called a SerDes technology, simply because

it supports differential, serial transmission of data, while in this book Ethernet is treated as a different technology (see Section 1.3 or Chapter 8).

One way to lessen the ambiguity around the term is to give what is being discussed as SerDes in this book a clear definition and a different name. The following thus defines "Automotive SerDes" with listing the properties commonly associated with "SerDes" in the automotive industry. While it might not always be explicitly spelled out, apart from in the previous Section 1.2.1, "SerDes" or "Automotive SerDes" throughout this book has the characteristics as listed below.

- a) It drives a wire.
- b) It supports "asymmetric communication", meaning high data rates in one communication direction (only).
- c) It supports Point-to-Point (P2P) communication (only).
- d) It supports the lowest two layers of the ISO/OSI communication model (only).

Ad a) Automotive SerDes drives a wire.

The electronics in cars are generally distributed. This is particularly true for sensors and displays, because they need to be at specific locations inside the car to fulfill their function. A lot of the sensing is done at the extremities of the body shell of a car, the displays need to be in alignment with the viewing positions from the seats. In contrast, processing units can be anywhere in the car where there is space and the right environment to put them. All units, however, need to communicate across copper or optical cables that can easily reach 10–15 m length. For installation in busses and trucks even 40 m are a typical requirement [7].

If a SerDes technology is used for sensors or displays, it thus has to be able to drive the respective cables, else it is not of interest for these use cases. Having cables and connectors available that support the high data rates in the challenging automotive environment, is therefore decisive for the success of the technology. See Chapter 5 for more details.

Ad b) Automotive SerDes supports high data rates in one communication direction (only).

SerDes communication is first of all unidirectional. The transmission direction goes from the serializing sender to the deserializing receiver. That SerDes allowed for unidirectional high data rates is how the technology was adopted in cars (see also Section 1.2.3); as it was usable for the one main transmit direction the sensor/video applications needed. For control, a separate, low data rate communication technology – for example the Local Interconnect Network (LIN) bus [8] – was used at the side to start with. It was then a matter of progress and cost reductions in semiconductor processing to optimize this set up. As a result, a bi-directional, low data rate control channel is now available with Automotive SerDes solutions. Naturally, the use cases would also work with symmetric highspeed communication. However, there is, generally, no need for the added complexity and costs, so Automotive SerDes solutions strived supporting high data rates in one transmit direction only.

"High" data rates are thereby relative and a matter of perspective. When the first cameras in cars used digital transmission technologies, the imagers might have had a Video Graphics Array (VGA) resolution of 640 × 480 (see Section 2.1.2 for details). With 30 frames per second (fps) and 16 bits color, this lead to about 150 Mbps data rate. At the

10

time, this was considered a very high data rate for in-vehicle communication. When the Media Oriented Systems Transport (MOST) bus was introduced at about the same time, it supported 25 Mbps [9], which again was a huge leap from the Controller Area Network (CAN) bus [10] or LIN available before. In 2021 in the automotive industry (and therefore also in this book), data rates larger than 1 Gbps were considered high. Data rates larger than 10 Gbps were considered to be "very high". In general, "very high" describes what is at the brink of feasibility at the time; also in this book.

Ad c) Automotive SerDes supports P2P communication (only).

At the physical layer, SerDes communication is P2P. This does not only mean that the SerDes link is not a bus, where more than two units would share the bandwidth, it also means that the complete SerDes communication starts at the one side of the communication and ends at the other, without extended networking capabilities. This suits especially camera and display use cases that only forward video data to or receive video data from the ECUs where the data is processed.

Occasionally, Automotive SerDes architectures are discussed that envision a daisy chain of cameras or displays (see also Section 2.1.3). This is generally done to save hardware in the processing ECU and/or to reduce the needed cable length. On the physical layer anyway, but also on the Data Link Layer (DLL) the communication still typically remains P2P between each display/camera and the processing ECU. The cameras/displays do not communicate among each other as would be possible if the communication was truly networked.

Ad d) Automotive SerDes supports the lowest two layers of the ISO/OSI communication model (only).

As the Automotive SerDes communication is P2P, the respective technologies generally comprise the PHYsical layer (PHY) and some DLL functions. This means that of the seven different communication functions defined in the ISO/OSI layering model [11], Automotive SerDes only covers layer one and two. This in return means that Automotive SerDes technologies do not need communication-specific software. Any particular requirements that might affect the software are related to the handling of the application specific protocols, which might be part of the Automotive SerDes products or the application data transported across the SerDes link, but not the Automotive SerDes technology itself (see Section 9.6 and Section 9.7 for more details on the protocols).

These are the general properties of "Automotive SerDes". Yet another terminology with ambiguities refers to the actual chip products that are often just called "Serializer" and "Deserializer". Figure 1.4 provides an overview. The term "Serializer (SER)" originally stands for the part that serializes and then transmits the data, the "Deserializer (DES)" for the part that receives and then deserializes the data. However, in modern Automotive SerDes technologies, the chip at the side of the communication that transmits the high data rate, also receives a smaller data rate for the control channel and the chip at the side that receives the high data rate also transmits a smaller data rate for control purposes. Both parts are, however, still called SER and DES. Furthermore, these now enhanced SERs and DESs can be integrated in a System on Chip (SoC) with the sensors, processing, or display control chips. They can also be part of stand-alone IVC bridge chips. In the automotive industry these bridge chips are also referred to as SER on the side that sends the high data rate and as DES on the side that receives the high data rate. This means, SER and DES might refer to three different sets of functionalities.

In order to reduce confusion, in this book, the bridge chip depicted in Figure 1.4 is called a "SerDes bridge", a "SER-bridge", or a "DES-bridge", depending on the context. Just SER or DES, describes the function on one or the other side of the communication link discussed, including a potential control channel. When, in the following text, exceptionally the original meanings of SER and DES are relevant, it is explicitly mentioned. Note that SerDes bridge chips can come in a number of flavors. These depend on the application specific protocols they bridge into, and also on the number of SERs and/or DESs they incorporate. Among other possible combinations, dual and quad DES-bridges are particularly common.



Figure 1.4 Different uses of the terms "SERializer (SER)" and "DESerializer (DES)"

One last note on the terminology. The terms "SerDes" and "Automotive SerDes" are a relatively new phenomena in the automotive industry. The industry tried other names, such as "High Speed Video Links (HSVL)" [12], "pixel links" [13], or, most commonly, "LVDS". Low Voltage Differential Signaling (LVDS) is a Serialization/Deserialization standard published in 1995 that combines low level signaling and differential communication (see also Section 7.2). It is often seen as the birthplace of SerDes and the early SerDes technologies used in the automotive industry were LVDS based. However, many modern Automotive SerDes technologies have nothing in common with the original LVDS. It is therefore no longer correct to use the term LVDS synonymously with Automotive SerDes. When the term "LVDS" is used within this book, it is used only when exactly LVDS is meant.

1.2.3 The Status of Automotive SerDes

The first time a SerDes technology was used in a series production car was in 2001. In its new 7-series, BMW used SerDes to connect the center display to the main infotainment, where the graphic data to be displayed was being rendered. The sources of original video data, such as cameras or a TeleVision (TV) receiver, were designed to be transferrable over analogue transmission systems. The graphic data for navigation systems was a new type of data that did not automatically cater for analogue transmission but required a high resolution on top. The SerDes technology used was the first Flat Panel Display (FPD) SerDes technology from National Semiconductor (now Texas Instruments, TI). The overall transmission

rate was about 500 Mbps using four wire pairs (three for data and one for the clock, see also Section 7.3.1) and a separate CAN connection for the control data.

Since then, the market has grown slowly but continuously. From 2005 on, Automotive SerDes solutions were even usable with dedicated, automotive suitable connectors; a fact not to be underestimated for the successful use of a communication technology (see Section 5.3.2 for more details). In 2021, the overall number of SerDes nodes in cars was expected to be about the same as the overall number of Ethernet nodes in cars [14]. The market growth had been accompanied by new features, such as higher data rates, integrated control channel, capabilities to transmit power with the data, support of coaxial cables and alike. Furthermore, more suppliers had entered the market, albeit offering their own non-interoperable, proprietary versions of Automotive SerDes solutions (see also Section 7.3 for technical details). And while the original FPD-Link technology was opened to be used by other semiconductor vendors, all follow up versions were also proprietary.

It is not so obvious, how the situation came about. After all, every technology used inside a car requires extra effort in terms of qualification (tools and test), logistics, and maintenance and that over many years (see also Section 3.1.2.2). If a car manufacturer decides to select just one supplier and technology to avoid multiplying the effort, the car manufacturer risks to be locked-in with a suboptimal technology down the road. This is because one vendor would need to supply the changing and growing portfolio alone, and it is unlikely that this one vendor will be the best choice for all chip variants needed. The monopolistic vendor might even lose the incentive to adapt and improve in the future. A living standard, for which a number of vendors is selling interoperable products, is the most desirable situation for a car manufacturer. It is likely optimized on various companies' core competences, entails an eco-system for tools, tests, cables, and alike, and is bound to be developed further for future versions.

So, why did this situation with various proprietary Automotive SerDes solution evolve? In the authors' opinion, it is a combination of the following two aspects: first, fast advancements of camera and display technologies that swept into the automotive industry from outside, and second, the connectivity was (is) P2P at the edge of the IVC network outside strategic decision making. Furthermore, camera and display applications have always had a large car user visibility. Up to know, only proprietary technologies were able to support the new features, as fast as the automotive industry wanted to use them. At the same time, it did not matter as much when proprietary technologies were used, especially, when the two units at each end of the communication link were provided by the same Tier 1 supplier in a closed system. The Tier 1 supplier offers exactly what the car manufacturer requires and looks for a cost optimized solution in order to win the contract. The car manufacturer also wants the best possible features available for its customers. As long as the costs work out, the incentive to push for a standard in such a scenario is limited.

While there might have been discussions on standardizing Automotive SerDes, until very recently though, they have not been followed through. There are a number of reasons, why the situation with respect to standardization has changed just now. First of all, it is a matter of sheer volume. The number of cameras and displays in cars is growing, while at the same time analogue connections for these applications are being phased out. Second, the car manufacturers are envisioning EE-architectures, in which cameras, high-speed sensors, and displays are bought from different Tier 1s than the ECU processing the data, potentially even with different time lines.

Index

Symbole

1-wire serial protocol 173 2.5GBASE-T1. See IEEE 802.3ch 4G 58, 214 4K 311, 315, 345 4-Pair Power over Ethernet. See 4PPoE 4PPoE, 4-Pair Power over Ethernet 165 5GBASE-T1. See IEEE 802.3ch 8B10B 208, 217, 222, 337, 342, 345 8K 25, 28, 32, 315, 345 8P8C 137 10BASE-T XVIII, 144 10BASE-T1S. See IEEE 802.3cg 10BASE-T2 143 10BASE-T5 143 10GBASE-T 273 10GBASE-T1, See IEEE 802.3ch 100BASE-T1. See IEEE 802.3bw 100BASE-TX XX, 16, 138, 142, 152, 165, 251 150 Ohm EMC test method 94 644A. See LVDS 1000BASE-RH. See IEEE 802.3bv 1000BASE-T 130, 133, 138, 146 1000BASE-T1. See IEEE 802.3bp

A

A2B, Automotive Audio Bus 316
Absorber-Lined Shielded Enclosure. See ALSE
Abstract Service Primitives. See ASP
AC-coupling 120, 166
ACK, ACKnowledge 225, 322
ACMD, A-PHY Control and Management Database 232
active matrix 26
adaptive AUTOSAR. See AUTOSAR
Adaptive Gain Controller. See AGC
ADAS, Advanced Driver ASsist functions 1, 55, 76, 377
AD, AUtonomous Driving 1, 55, 70, 80
Additive White Gaussian Noise. See AWGN

Advanced Driver ASsist functions See ADAS Advanced Video Coding. See H.264 AEC, Automotive Electronics Council 82 AEC-Q100 XVIII, 82 AEC-Q101 82 AFDX, Avionics Full-Duplex Switched Ethernet 284 AFEXT. See crosstalk after sales 74 AGC, Adaptive Gain Controller 221, 237, 261, 274, 360 agile development 358 aging effect 76 alien crosstalk. See crosstalk ALSE, Absorber-Lined Shielded Enclosure 97, 219 Always-On Sentinel Conduit. See AOSC AMEC 151 American Wire Gauge. See AWG AML. See ASAML ANEXT. See crosstalk ANSI/TIA/EIA-644A. See LVDS antennas 50, 58, 97, 124, 17, 141, 143 - antenna tests 97 - horn antenna 97 - LogPer antenna 97 - periodic broadband antenna 97 - rod antenna 97 AOSC, Always-On Sentinel Conduit 330 A-PHY. See MIPI A-PHY A-PHY Control and Management Database. See ACMD API, Application Programming Interface 41 APIX, Automotive Pixel Link 18, 210 APPI, A-PHY Protocol Interface 215 Application Programming Interface. See API Application Stream Encapsulation Protocol. See ASEP Arbitrary Waveform Generator. See AWG architecture. See EE-architecture ARQ. See retransmissions Arrhenius equation 149 ASA, Automotive SerDes Alliance XXI, 13, 45, 84, 232 ASAML, ASA Motion Link XXI, 45, 133, 232, 291, 316 - Branch 232, 241

- channel 234 - DLL 240 - keys for security 241 - Leaf 232 - PCS 239 - PMA 237 - Root 232 - security 45, 241 ASA Motion Link. See ASAML ASEP, Application Stream Encapsulation Protocol 232, 237, 243 ASIC 40 ASIL. See functional safety ASP, Abstract Service Primitives 368 asymmetric communication 9, 52, 58, 199, 269, 288, 289 asymmetric Ethernet 268 ATS, Asynchronous Traffic Shaping 281 ATSC, Advanced Television Systems Committee XX, 24, 25 attenuation. See IL Audio Video Bridging. See AVB Automatic Retransmission/Repeat reQuest. See retransmissions Automotive Audio Bus. See A2B Automotive Electronics Council. See AEC automotive environment 67 Automotive Ethernet. See Ethernet automotive market 67 Automotive Open System Architecture. See AUTOSAR Automotive Pixel Link. See APIX Automotive Safety Integration Level. See functional safety Automotive SerDes. See SerDes Automotive SerDes Alliance. See ASA autonegotiation 268 Autonomous Driving. See AD AUTOSAR, Automotive Open System Architecture XXIX, 41, 275, 286 - AUTOSAR SecOC XXIX - SecOC 286 auxiliary channel 30, 342, 343 AV1, compression format 311 AVB, Audio Video Bridging XX, 276 AVC. See H.264 Avionics Full-Duplex Switched Ethernet. See AFDX AVNU Automotive Profile 277 AWG, American Wire Gauge 138 AWG, Arbitrary Waveform Generator 377 AWGN, Additive White Gaussian Noise 228, 377

В

balance. *See* symmetry bandwidth reservation **283** Bayer pattern filter **37** BCI, Bulk Current Injection 94, 96, 219, 236, 293 - BCI test and ESD components 115 BER, Bit Error Rate 42, 119, 229, 258, 291, 360, 375 best effort transmission 276 Best Master Clock Algorithm. See BMCA BGA package 83 Bias-T 166, 172, 174, 178, 186, 205, 207, 375 Bill Of Material. See BOM BIST, Built-In Self-Test 189, 232, 359, 365 Bit Error Rate. See BER black box testing 366 blanking 27, 43, 169, 195, 201, 240, 331, 335, 342 BMCA, Best Master Clock Algorithm 280 BNC connector 201 BOM, Bill Of Material 206 boundary scan test 363 branch coverage 366 bridge 5, 11, 15 broadcast 232, 289 BroadR-Reach XX. 138. 251 BTA. Bus TurnAround 331 buck-boost converters 168, 184 built-in error generator 365 Built-In Self-Test. See BIST Bulk Current Injection. see BCI Bus TurnAround. See BTA ByteFlight 264

С

Cable Discharge Event. See CDE cabling 2, 9, 16, 18, 58, 76, 139, 180, 292, 347 - aging 150 - bending 144 - diameter 139 - hybrid 147 length requirement 9 - multi-port 145 parameters for copper cables 120 predefined 122 - stress types 149 Call for Interest. See CFI Camera Command Set. See MIPI CCS cameras XVII, XIX, 4, 34, 56, 78, 79, 169, 172, 173, 182, 188, 192, 200, 204, 205, 214, 240 - elements 39 - lens 36, 39 - software 41 Camera Service Extension. See MIPI CSE CAN 10, 12, 46, 141, 152, 166, 194, 199, 201, 204, 286 Capacitive Clamp Coupling method. See CCC method car variants 69 CAT 5e 138, 146 CAT 6a 138, 146 CAT cables 137

Cathode-Ray Tube. See CRT display CBS, Credit Based Shaper 281 CCC method, Capacitive Clamp Coupling method 98 CCD, Charge-Coupled Device XVII, 34 CCS. See MIPI CCS CD, Compact Disc XVII. 304 CDE, Cable Discharge Event 109 CDM, Charged Device Model 108 CEC, Consumer Electronics Control 338 center-stack display 23 centralized network management 283 CFA, Color Filter Array 37 CFI, Call For Interest XXI, 253, 261, 264 channel 119, 217, 234, 253, 265, 373 - definition 120, 217, 234, 253, 261 - impairments 126 - responsibilities in the value chain 121 - specification 137 - testing 373 channel monitor loop 361 Charge-Coupled Device imager. See CCD Charged Device Model. See CDM chirp 50 CIA, Confidentiality, Integrity, Availability 43 cinch connectors 201 CISPR XVI, 255 classic AUTOSAR. See AUTOSAR clock 209, 315, 317, 320, 331, 347 - clock generation 211 - clock master 216 - inter-pair skew 130 intra-pair skew delay 131 - leader 237 - skew 8, 263 closed loop method 96 CMC, Common Mode Choke 120, 172 CML, Current Mode Logic 204, 210 CMOS, Complementary Metal-Oxide Semiconductor XVIII, 34, 213 CMOS imager 34, 36, 55 CMYK 304 CO2 emissions XXI, 77 coaxial cables 12, 13, 40, 100, 119, 123, 138, 143, 217, 234, 288 color filter 36 Color Filter Array. See CFA color resolution. See resolution, color Color, Video, Blanking, and Synchronization. See CVBS Common Mode Choke. See CMC Compact Disc. See CD complexity 68, 269, 288, 291 compliance 84, 366, 367, 372, 375 compression 4, 16, 28, 30, 204, 251, 305, 341 Confidentiality, Integrity, Availability. See CIA

conflict between ESD, EME and EMI 115 Conformance Test Specification. See CTS connecting process 150 connectors 76, 150, 347 - parameters 150 - predefined 122 - variants 150 consumer device integration 31 Consumer Electronics Control. See CEC content protection XVII, 43, 313 Continuous-Time Linear Equalizer. See CTLE Continuous Wave, See CW control channel 9, 29, 33, 40, 44, 49, 201, 203, 208, 210, 222, 234, 338 Controller Area Network. See CAN cooperation among car manufacturers 72 costs 2, 9, 12, 23, 45, 56, 68, 70, 80, 81, 165, 320, 355 coupling - capacitive 91, 132 - clamp coupling methods 98 - conducted 91, 136 - far-field 91 - inductive 91.132 - methods 98 parasitic resistive 132 coupling attenuation 131, 218, 254, 262 CRC, Cyclic Redundancy Check 14, 205, 224, 226, 241 Credit Based Shaper. See CBS crosslink PolyEthylene. See PE-X crosstalk 8, 132, 145, 218, 234, 254, 262, 294 - alien XTALK 133, 136, 294 CRT display, Cathode-Ray Tube display 22, 27, 334 CSI-2. See MIPI CSI-2 CTLE, Continuous-Time Linear Equalizer 332 CTS, Conformance Test Specification 372 Current Mode Logic. See CML CVBS, Color, Video, Blanking, and Synchronization 199, 200 CW, Continuous Wave 50 cyclic queuing and forwarding 284 Cyclic Redundancy Check. See CRC

D

D2B, Domestic Digital Bus 264 daisy-chain 31, 44, 212, 213, 233, 277, 341, 343 Dark Signal Non-Uniformities 38 Data Link Layer. *See* DLL data loggers 376 data rate adaptation 195, 225 DCC method, Direct Capacitive Coupling method 98 DC-DC converters 167 DC resistance 120, 167, 179, 180 DDC, Display Data Channel 334, 337, 341, 347 decentralized network management 283 deep packet inspection 363 deep sleep 192, 232, 241, 272 defect rate 81 de-mosaicing 39 Denial of Service. See DoS deserializer 6, 11, 206, 209, 211, 213, 245 development cycle 73 Device Under Test. See DUT dielectric losses 126 dielectric strength 105 Dieselhorst-Martin stranding. See DM stranding differential transmission 8, 124, 201, 262 differentiation among car manufacturers 69 Digital Millennium Copyright Act. See DMCA Digital Stream Compression. See DSC digital TV. See TV, digital or DVB Digital Video Broadcasting. See DVB Digital Visual Interface. See DVI Direct Capacitive Coupling method. See DCC method Direct Memory Access. See DMA Direct Power Injection. see DPI display 4, 21, 166, 192, 200, 203, 210, 215, 244 - elements 30 - formats 25 - protocols 333 Display Data Channel. See DDC DisplayPort. See DP Display Service Extension. See MIPI DSE dithering 26, 238 DL, DownLink 32, 234 DLL, Data Link Layer 10, 14, 230, 240, 272 DMA, Direct Memory Access 319 DMCA, Digital Millennium Copyright Act XIX, 313 DM stranding, Dieselhorst-Martin stranding 145 Domestic Digital Bus. See D2B Doppler effect 47, 50 DoS, Denial of Service 282, 286 DownLink. See DL DP, DisplayPort XX, 122, 244, 341, 347 - DP++, dual-mode transmission 344 D-PHY. See MIPI D-PHY DPI, Direct Power Injection 94, 95 DSC, Display Stream compression 28, 311 DSI-2. See MIPI DSI-2 DSI3 46 DSNU, Dark Signal Non-Uniformities 38 dual homing 285 dual-view display 24 duplexing methods 288 DUT, Device Under Test 366, 373 DVB, Digital Video Broadcasting XX, 24, 25, 308 DVB-T2 XX, 308 DVD XVIII, 308 DVI, Digital Visual Interface XIX, 335, 347

Е

echo canceller 261, 270, 274 echo strength. See RL EDID, Extended Display Identification Data 334, 338 eDP, embedded DisplayPort XX, 210, 244, 341, 343, 347 EE-architecture 2, 5, 12, 29, 39, 47, 55, 57, 70, 78, 210, 213, 251, 253, 277, 358 EEE, Energy Efficient Ethernet XX, 196, 257, 269, 272, 273 EFM, Ethernet in the First Mile 268 electrical breakdown 105 electric ground. see GND ElectroMagnetic Compatibility. See EMC ElectroMagnetic Emissions. see EME ElectroMagnetic Immunity 94. see EMI electromagnetic waves 47, 48 electrostatic charge levels 106 ElectroStatic Discharge. See ESD embedded DisplayPort. See eDP EMC, ElectroMagnetic Compatibility XVI, 79, 89, 207, 228, 236, 255, 293 - sensitive systems 93 - specification 92 - test order 93 variants of interference 91 EME, ElectroMagnetic Emissions 89, 94 EMI, ElectroMagnetic Immunity 89 emissions gap 134 End-Node-Interconnect-Structure. See ENIS (MDI network) End Of Production. See EOP Energy Efficient Ethernet. See EEE ENIS (MDI network), End-Node-Interconnect-Structure 217 environmental safety 255 EOP, End Of Production 74, 75 EPON, Ethernet Passive Optical Network 268 EPROM, Erasable Programmable Read-Only Memory 39, 378 Equivalent Series Resistance. See ESR ESD, ElectroStatic Discharge 105, 374 diode snapback 112 - ESD diodes 112, 120 - ESD diodes, uni- and bidirectional 112 - ESD, powered 110 - ESD protection 112 - ESD protection, chip internal 114 - ESD, unpowered 106 protection parameters 113 -- test voltages 107 ESR, Equivalent Series Resistance 169 Ethernet XVII, 13, 17, 33, 41, 46, 52, 53, 57, 58, 199, 211, 228, 244, 251 Ethernet camera 40

Ethernet in the First Mile. *See* EFM Ethernet Passive Optical Network. *See* EPON Ethernet side channel **210** Ethertype **14, 278** EU, European Union **35, 79** evaluation boards **372** Extended Display Identification Data. *See* EDID extinctions **157** extreme programming **359**

F

FAKRA 152 Farb-Bild-Austast-Synchron-Signal. See CVBS Far-End CrossTalk. See FEXT fast transients 135, 219, 236 FBAS. See CVBS FDD, Frequency Division Duplexing 174, 178, 204, 207, 217, 270, 288 FEC, Forward Error Correction 137, 228-230, 234, 237, 241, 258, 268, 271 ferrite beads 176 FEXT, Far-End CrossTalk 133 Fiber Optical Transmitter. See FOT flash Lidars 53 FlexRay 46, 141, 199, 286, 369 FMCW, Frequency Modulated CW 50, 53 FoFa, Forwarding Fabric 233 Forward Error Correction. See FEC Forwarding Fabric. See FoFa FOT, Fiber Optical Transmitter 264 FPD-Link XIX, 12, 130, 133, 203 FR-2 157 FR-4 158 frame preemption 284 Frame Rate Control. See FRC frame replication 284 FRC, Frame Rate Control 26 Frequency Division Duplexing. See FDD Frequency Modulated CW. See FMCW FRR, Front-Range Radar 51 full-duplex XIX, 13, 129, 175, 205, 264, 270, 272, 288 functional safety 33, 42, 79, 224, 241

G

Gamma correction 26, 39 Gamma sequence 33 gamut 26 generalized Precision Time Protocol. See gPTP General Purpose Input/Output. See GPI/O Gigabit Multimedia Serial Link. See GMSL Gigabit Video InterFace. See GVIF GigaSTAR 210 GI-POF, Graded Index POF 265 Glass Optical Fiber. See GOF global shutter 37 GMSL, Gigabit Multimedia Serial Link 207 GND, ground 90, 100 - car body GND 103 - GND shift 103, 172 ground loops 103 - recommended ECU GND 112 GOF, Glass Optical Fiber 288 golden device 367 GPI/O 33, 205, 209, 244, 317 GPS navigation. See navigation system gPTP, generalized Precision Time Protocol 279 GPU, Graphics Processing Unit 29, 30 Graded Index POF. See GI-POF grandmaster clock 280 Graphics Processing Unit. See GPU Gray code 223, 237, 259 ground. See GND GVIF, Gigabit Video InterFace 212

Н

H.262 XVIII. 308, 310 H.264 XX, 303, 308, 310, 312 H.265 308, 310, 312 half-duplex 13, 129, 234, 272, 332 HBM, Human Body Model 108 HDBASE-T 140, 217 HDCP, High-bandwidth Digital Content Protection XIX, 31, 205, 313, 338 HDMI, High Definition Multimedia Interface XIX, XX, 29, 122, 206, 210, 337, 347 HDR, High Dynamic Range 33, 37 HDTV, High-Definition Television 25 HEIF, High-Efficient Image Format 307 HEVC. See H.265 HFM, High-speed FAKRA Mini 153 High-bandwidth Digital Content Protection. See HDCP High Definition Multimedia Interface. See HDMI High-Definition Television. See HDTV High Dynamic Range. See HDR High Efficiency Image File format. See HEIF High Efficiency Video Coding. See H.265 High-Speed Data cables. See HSD cables High-Speed Data connectors. See HSD connectors High-speed FAKRA Mini. See HFM High-speed Modular Twisted-pair Data connector. See H-MTD connector High Speed Video Links. See HSVL HMI, Human Machine Interface 3 H-MTD connector, High-speed Modular Twisted-pair Data connector 151, 152 H-MTDe connector 151, 152 horizontal blanking 27 housing. See package

HSD cables 212 HSD connectors, High-Speed Data connectors 152 HSVL, High Speed Video Links 11 Human Body Model. *See* HBM Human Machine Interface. *See* HMI human vision 3, 34, 40 hybrid 270, 274

I

I2C XVII, 29, 33, 40, 204, 205, 207, 244, 320, 333, 338 - ACK mechanism 323 - addressing 323 - channel access 322 - multi controller system 323 - PHY 321 - transmission modes 321 I2C bulk mode 326 128 XVII. 30, 244, 315 I3C. See MIPI I3C IBG, Inter-Burst Gap 237 ICC method, Inductive Coupling/Current Clamp method 98 ICs, Integrated Circuits 6, 81 ICT, In-Circuit Testing 83 IEEE 802.1 15, 276 IEEE 802.1AS 279 IEEE 802.1CB 284 IEEE 802.1Q XIX, 14 IEEE 802.1Qat 283 IEEE 802.1Qav 281 IEEE 802.1Qbu 284 IEEE 802.1Qbv 283 IEEE 802.1Qca 284 IEEE 802.1Qcc 283 IEEE 802.1Qch 284 IEEE 802.10ci 282 IEEE 802.1Qcr 281 IEEE 802.3az. See EEE IEEE 802.3bp 18, 138, 140, 141, 151, 152, 172 IEEE 802.3br 284 IEEE 802.3bu 172 IEEE 802.3bv 18, 264 IEEE 802.3bw XX, 16, 18, 138, 139, 151, 152, 172, 199 IEEE 802.3cg 18, 199, 319 IEEE 802.3ch XXI, 18, 133, 140, 252, 255, 291-294 - PCS 257 - PMA 261 - test modes 370 IEEE 802.3cy XXI, 18, 261, 291-294 - PCS 263 IEEE 802.3cz XXI, 18, 263

IEEE 802.3dh 265 IEEE 1149 363 IEEE 1394 Firewire 152 IEEE 1722 15. 278 IEEE 2977. See MIPLA-PHY IEEE P802.1DG 277 IET. Interspersing Express Traffic 284 IL, Insertion Loss 126, 135, 144, 145, 149, 155, 234, 254.261 imager, image sensors 35, 36, 39 Image Signal Processor. See ISP immunity gap 135 impedance 91, 94, 95, 100, 123, 144, 149, 209, 210, 217, 234 - in the power path 175 - mismatch 115, 123 Improved Inter-IC bus. See MIPI I3C In-Circuit Testing. See ICT Inductive Coupling/Current Clamp method. See ICC method inductor 176 Industrial, Scientific, and Medical frequency band. See ISM band infotainment 21 InfraRed. See IR ingress policing and filtering 282 innovation 70 input current 168 inrush current 169, 190 Insertion Loss. See IL insulation material 156 insurance 68.80 Integrated Circuits. See IC Integrated Services Digital Broadcasting. See ISDB intelligence 4, 29, 41, 70 Inter-Burst Gap. See IBG interface latency 327 interference model 134 interference types 137 interfering noise. See noise types Inter-IC bus. See I2C Inter-IC Sound bus. See 12S Inter-Integrated Circuit. See I2C interleaving 257, 262, 271, 293 Internet Protocol. See IP interoperability 84 inter-pair skew. See clock Interspersing Express Traffic. See IET InterSymbol Interference. See ISI IP, Internet Protocol 286 IPsec XXXVII, 286 IR, InfraRed 35, 48 ISDB, Integrated Services Digital Broadcasting XX, 24, 25 ISI, InterSymbol Interference 129

ISM band, Industrial, Scientific, and Medical frequency band 50 ISO 9646 368 ISO 26262. See functional safety ISO/OSI layers XVII, 14 ISP, Image Signal Processor 38, 39 IVC bridge. See bridge

J

JEIDA, Japan Electronic Industry Development Association 212 JEITA, Japan Electronic and Information Technology industries Association 212 JITC, Just-In-Time Canceller 226 JPEG, Joint Photographic Experts Group 40, 305 JTAG, Joint Test Action Group 363 Just-In-Time Canceller. See JITC

К

key exchange 43, 241

L

laminated paper. See FR-2 laptop market 23, 202 layer 2 bridge. See switch LCD, Liquid Crystal Display 22, 24, 29, 202, 334, 345 LCL, Longitudinal Conversion Loss 131 legislation 77, 78 licensing 212, 311, 312, 314, 347 Lidar XXI, 52, 56 lifetime 76 light sleep 194, 196, 240, 272 Linear Shift Feedback Register. See LSFR linear voltage regulators 167, 184 Line Impedance Stabilization Network. See LISN link adaptation 225 link length 121 link margin 155 LIN, Local Interconnect Network 9, 46, 101, 199, 201, 286 lip synchronization 30, 33 Liquid Crystal Display. See LCD LISN, Line Impedance Stabilization Network 96 Listener 277 load changes 136 Local Interconnect Network. See LIN Longitudinal Conversion Loss. See LCL Long-Range Radar. See LRR Long Term Evolution. See LTE loopback testing 364 lossless compression 305 lossy compression 305, 310 lower tester 368

Low Power Idle. *See* LPI low power modes **191** Low Voltage Differential Signaling. *See* LVDS LPI, Low Power Idle **273** LRR, Long-Range Radar **51** LSFR, Linear Shift Feedback Register **237, 258** LTE, Long Term Evolution **58, 214** LV 124 **136** LVDS, Low-Voltage Differential Signaling **XVIII, 11, 199, 201**

Μ

Machine Model. See MM machine vision 3.34 Macrovision content protection 313 MACsec 45, 285 maintenance 68 Management Data Clock. See MDC Management Data Input/Output. See MDIO MASS, MIPI Automotive SerDes Solution 329 MateAX 153 MateNet 151 maximum current 179 MC, Mode Conversion 125, 126, 131 MDC, Management Data Clock 256 MDI, Media Dependent Interface 40, 120 - MDI insertion loss 156 - network 120, 156 - MDI return loss 40, 121, 156 MDIO, Management Data Input/Output 256 Mean Square Error. See MSE mechanical stress 148 Media Dependent Interface. See MDI Media Independent Interface. See MII Media Oriented Systems Transport. See MOST bus memory map 325, 328, 329 MGBASE-AU. See IEEE 802.3cz MGBASE-T1. See IEEE 802.3ch or IEEE 802.3cv Micro Quadlok System. See MQS micro-reflections 130, 262 microstrip 158, 331 Mid-Range Radar. See MRR MII, Media Independent Interface 14, 251 MIMO, Multiple Input Multiple Output 51 Mini-coax 153 MIPI Alliance XX, 13, 84, 214 MIPI A-PHY XXI, 214, 291 - channel 217 - Profile 1 217, 224 - Profile 2 217, 224 MIPI Automotive SerDes Solutions. See MASS MIPI Camera Service Extension. See MIPI CSE MIPI CCS, Camera Command Set 41, 333 MIPI C-PHY 215, 331

- dual-mode transmitter 332

MIPI CSE, Camera Service Extension 232, 333 MIPI CSI-2 XX, 206, 214, 329, 330 MIPI DCS, Display Command Set 341 MIPI Display Service Extension. See MIPI DSE MIPI D-PHY 206, 214, 331 MIPI DSE, Display Service Extension 232, 341 MIPI DSI-2 XX. 206. 347 MIPI I3C 324, 333 MJPEG, Motion JPEG 40, 307 MMF, MultiMode Fiber 265 MMIC, Monolithic Microwave Integrated Circuits 51 MM. Machine Model 107 mobile communication 58, 214 mobile device integration 31 Mode Conversion. See MC modulation 155 Monolithic Microwave Integrated Circuits. See MMIC MOST bus, Media Oriented Systems Transport bus 10, 152, 252, 264 Motion IPEG. See MIPEG moving cycles 76 Moving Pictures Experts Group. See MPEG MPEG-2. See H.262 MPEG-4. See H.264 MPEG, Moving Pictures Experts Group XVIII, 305, 307.308 MQS, Micro Quadlock System 151 MRR, Mid-Range Radar 51 MSE, Mean Square Error 360 MST, Multi-Stream 342, 343 MTP, Multi-stream Transport Packet 344 multi-beam Lidars 53 multicast 31, 44, 232, 240, 289 MultiMode Fiber. See MMF Multiple Input Multiple Output. See MIMO multipoint MAC control 268 Multi-Stream (DP/eDP). See MST Multi-stream Transport Packet. See MTP

Ν

nACK 323 Narrow Band Interference. *See* NBI National Television System Committee. *See* NTSC navigation system XVII, 22, 23 NBI, Narrow Band Interference 228, 236, 293 NCAP, New Car Assessment Program 80 Near-End CrossTalk. *See* NEXT Near Field Communication. *See* NFC network congestion 282 New Car Assessment Program. *See* NCAP NEXT, Near-End CrossTalk 133 - PSANEXT 133 NFC, Near Field Communication 141 nMQS, nano MQS 151 NodelD 233 noise floor 134 noise generator 365 noise types 135 NRZ, Non-Return to Zero 120, 207, 208, 210, 214, 217, 234, 236, 267 NTSC, National Television System Committee 25, 200, 334 Nyquist frequency 127, 221, 234, 254

0

OAM, Operation, Administration, and Management 240, 241, 257, 262 OFDM, Orthogonal Frequency Division Multiplexing 50 OLED, Organic Light Emitting Diodes 22, 24 OM3 265 One-Time Programmable memory. See OTP OPEN Alliance 18, 84, 272, 319, 371 OpenLDI XIX, 206, 209, 211, 213, 335, 347 open-load 170 Operation, Administration, and Management channel. See OAM optical media 263 Organic Light Emitting Diodes. See OLED Orthogonal Frequency Division Multiplexing. See OFDM oscilloscope 375 OTP, One-Time Programmable memory 39, 379 outsourcing 69 over-voltage 170

Ρ

P2P communication 10, 32, 44, 58, 213, 232, 244, 276, 277, 289 pacer 226 package 83 Packet Error Rate. See PER PAL, Phase Alternation Line 25, 200 PAL, Protocol Adaptation Layer 215, 230, 330 PAM 2. See NRZ PAM 4 208, 234, 259, 262, 267 PAM 16 223 PAM, Pulse Amplitude Modulation 120, 221, 223, 226, 234 parallel data transmission 7 parking aid systems 49 partial networking 77, 194 Parts Per Million. See PPM pattern generator 377 PCB, Printed Circuit Board 7, 83, 120, 156 - material 157 PCIe, Peripheral Component Interconnect express 122

PCM, Pulse Code Modulation 315 PCS, Physical Coding Sublayer 14, 220, 222, 236, 239, 257, 263, 267 pDelav measurement 280 PD, Powered Device 136, 166, 167, 182 PDU, Protocol Data Unit 368 Peripheral Component Interconnect express. See PCIe Peripheral Sensor Interface five. See PSI5 PER, Packet Error Rate 229 PE-X, crosslink PolyEthylene 139 phantom power 165 Phase Alternation Line. See PAL PHY 119, 220, 236, 255, 265, 321 PHY Protocol Interface. See PPI, PHY Protocol Interface Physical Coding Sublayer. See PCS Physical Layer Signaling. See PLS Physical Media Attachment. See PMA Physical Media Dependent. See PMD PICS, Protocol Implementation Conformance Statement 366 picture element. See pixel pixel 24, 25, 36 - size 36 pixel links 11 Pixels Per Inch. See PPI Plastic/Polymer Optical Fiber. See POF PLC, Product Life Cycle 73 PLS, Physical Layer Signaling 269 PMA, Physical Medium Attachment 14, 220, 237, 261 PMD, Physical Media Dependent 220, 264 PoC, Power over Coaxial 40, 136, 173, 204 - manual 186 - stability 182 PoDL, Power over DataLine 172 PoD, Power over Differential cables 171 PoE, Power over Ethernet 165 POF. Plastic Optical Fiber 265 Point-to-Point. See P2P communication PolyEthylene, crosslink. See PE-X PolyPropylene. See PP PolyVinyl Chloride. See PVC power - consumption 4, 38, 40, 53, 68, 77, 164, 291 - load change 169 - modes 188,190 - power ripples 135, 169, 236, 292 - savings 77, 164, 188, 191 - savings potential 274 - supply 30, 40, 49, 77, 152, 164, 188 - supply limits 170 - supply path 179 Powered Device. See PD power-over XXI, 165, 166, 235, 288 - limit 179

Power over Coaxial. See PoC Power over DataLine, See PoDL Power over Differential cables, See PoD Power over Ethernet. See PoE Power Sourcing Equipment. See PSE Power Spectral Density. See PSD Power Sum Alien Attenuation to Crosstalk Ratio Far-end, See FEXT Power Sum Alien Near-End crossTalk. See NEXT PPI, PHY Protocol Interface 332 PPI, Pixels Per Inch 26 PPM. Parts Per Million 81 PP, PolyPropylene 139 PRBS, Pseudo-Random Bit Sequence 208, 238 Precision Time Base. See PTB pre-processed data 199 presentation time 278 Printed Circuit Board. See PCB privacy 43, 242 production 69 Product Life Cycle. See PLC Protocol Adaptation Layer. See PAL protocol analyzers 377 Protocol Data Unit. See PDU Protocol Implementation Conformance Statements. See PICS PSAACRF. See FEXT PSAACRF. Power Sum Alien Attenuation to Crosstalk Ratio Far-end 133 PSANEXT. See NEXT PSD, Power Spectral Density 121, 219, 236 PSE, Power Sourcing Equipment 136, 166, 167, 182 Pseudo Random Bit Sequence. See PRBS PSI5 46, 101, 199 PTB, Precision Time Base 237, 238 Pulse Amplitude Modulation. See PAM Pulse Code Modulation. See PCM Pulse-Width Modulation. See PWM PVC, PolyVinyl Chloride 139 PWM, Pulse-Width Modulation 29, 101, 135

Q

QAM, Quadrature Amplitude Modulation 200 QFN package 83 QoS, Quality of Service XX, 4, 15, 57, 276 Quadrature Amplitude Modulation. See QAM quality 81 Quality of Service. See QoS quiescence current 77

R

radar XVI, XIX, 50, 56 - cube 51 radio XVI, 22, 58 Radio Corporation of America. See cinch connectors Radio Frequency. See RF raw data 199 RBP, Reverse Battery Protection 171 RCA, Reverse Channel Audio 339 - RCA. See cinch connectors Real-time Transport Protocol. See RTP Rear Seat Entertainment. See RSE recall 355 receiver input 135 Red Green Blue. See RGB Reed Solomon FEC. See RS-FEC or FEC reference clock 206 reference time 279 regulation 77, 79 relation between IL and RL 128 resistive losses 126 resolution 25, 32 - color 9,26 resonances 157 retrain 227 retransmissions 137, 212, 217, 220, 224, 227-230 Reverse Battery Protection. See RBP Reverse Channel Audio. See RCA RF ingress 219, 236, 293 RF, Radio Frequency 135 RGB, Red Green Blue XVI, 26, 200, 206, 302, 329 RGGB. See Bayer pattern filter ringing effect 170 ripples. See power, power ripples RI-45 137 RL 126, 128, 234, 254 rolling shutter 37 RSE, Rear Seat Entertainment XIX, 58 RS-FEC 228-230, 234, 237, 258, 262, 268 RTP, Real-time Transport Protocol 279

S

SA, Screening Attenuation 126, 131, 148, 254 SATA, Serial Advanced Technology Attachment 122 Scalable service-Oriented MiddlewarE over IP. See SOME/IP scanlines 363 scanning Lidars 53 SCART connector 201 Scattering parameters. See S-parameters SCCP, Serial Communication Classification Protocol 173 SCI, Sub-Constellation Index 226 scrambler 205, 208, 214, 222, 237, 258, 268 Screening Attenuation. See SA scrum 359 SDL, System and Description Language 369 SDP, Shielded Differential Pair 143, 152

seamless redundancy 284 SecOC. See AUTOSAR security 33, 43, 241, 285, 292 semiconductor market 71 semiconductor quality 81 semiconductor supply 84 semiconductor vendor 72 SenseWire, See MIPLI3C sensors 4,46 - architecture 57 - dumb 4 intelligent 4 SENT, Single Edge Nibble Transmission 46, 101, 199 SEPIC, Single-Ended Primary-Inductor Converter 168, 184 SEP, Service Extension Packet 333 SerDes 6, 9, 16, 52, 199, 289, 333 Serial Advanced Technology Attachment. See SATA Serial Communication Classification Protocol. See SCCP serializer 6, 11, 206, 209, 211, 213, 245 Serial Peripheral Interface. See SPI Service Extensions Packet. See SEP SFCW, Stepped Frequency CW 50 Shield Attenuation. See SA Shielded Differential Pair. See SDP Shielded Parallel Pair. See SPP Shielded Twisted Pair. See STP Shielded Twisted Quad cables. See STQ shielding 99, 141 - 360-degree 102 - case shielding 102 short-load 170 Short-Range Radar. See SRR shutter 37 signal propagation delay 127 Signal Quality Indicator. See SQI Signal to Noise Ratio. See SNR Single Edge Nibble Transmission. See SENT single-ended communication 8 Single-Ended Primary-Inductor Converter. See SEPIC SI-POF, Step Index POF 265 skew. See clock, skew skin-effect 156 sleep modes. See light sleep or deep sleep sleep signal 273 slow transients 135 slow wake 273 Smart Region Of Interest. See SROI SNR, Signal to Noise Ratio 100, 120, 155, 221, 241, 360 SOME/IP, Scalable service-Oriented MiddlewarE over IP 41 sonar XVII, 48, 56 - ECU 49 sonic 47

sonic waves 48 SOP, Start Of Production 73 SOVS, System Operational Vector Space 369 space considerations 78 S-parameters, scattering parameters 124 spectrum analyzer 376 speed of an electrical signal 157 SPI, Serial Peripheral Interface 29, 33, 40, 205, 244, 317 SPP, Shielded Parallel Pair 119, 142, 261 SQI, Signal Quality Indicator 360 sRGB XIX. 302 SROI, Smart Region of Interest 330 SRP, Stream Reservation Protocol 283 SRR, Short-Range Radar 51 standard 13, 15, 244 STar Quad cables. See STQ Start Of Production. See SOP star topology 31 start-up 77, 240 status registers 360 step-down converters 168, 184 Step Index Plastic/Polymer Optical Fiber. See SI-POF Stepped Frequency CW. See SFCW step-up converters 168, 184 stereo cameras 40 STP, Shielded Twisted Pair 101, 119, 123, 138, 141, 288 STP, Shieled Twisted Pair 100 STQ, STar/Shielded Twisted Quad cables 119, 141, 145, 210, 212, 226 stream blocking 282 stream ID 278 Stream Reservation Protocol. See SRP stripline measurement 94, 95 stripline (PCB design) 158 Sub-Constellation Index. See SCI substitution method 97 suck-out 141 supply chain 69, 84 surround view system 34 switch 15 switching noise 136, 169 symmetry 101, 131 System and Description Language. See SDL System Operational Vector Space. See SOVS

Т

tag coverage 366 Taguchi's method 370 Talker 277 TAS, Time Aware Shaper 283 TC 10 272, 275 TCL, Transverse Conversion Loss 131 TCON, Timing CONtroller 29, 30 TCP/IP XVIII, 15, 290 TCP, Transmission Control Protocol XVII, 286 TDD, Test-Driven Development 359 TDD, Time Division Duplex 239, 268, 270, 274, 304, 322 TDR, Time Domain Reflectometry 148, 236, 374 television. See TV temperature requirements 76.82 testability 359 Test-Driven Development. See TDD test metrics 366 test output 361 Test Points. See TP test specifications 367 TFT, Thin Film Transistor 26 thermal breakdown 105 thermal noise 133 Tier 1 69, 70, 81 Time Aware Shaper. See TAS Time Division Duplex. See TDD Time Domain Reflectometry. See TDR Time of Flight. See ToF timeouts 241 Time Sensitive Networking. See TSN Timing CONtroller. See TCON TLIS (link segment) 217 TLP, Transmission Line Pulse measurement 109 TLS, Transport Layer Security 286 TMDS, Transition-Minimized Differential Signaling 337 ToF, Time of Flight 35 - camera 54, 56 - Lidar 53, 54 Token Ring 16 touch screen XVII, XVIII, 23, 29, 33 TP, Test Points 120 traffic shaping 281 transients. See fast and slow transients Transient Voltage Suppression. See TVS Transition-Minimized Differential Signaling. See TMDS Transmission Control Protocol. See TCP Transmission-Line-Interconnect-Structure. See TLIS (link segment) Transmission Line Pulse measurement. See TLP transmitter output 135 Transport Layer Security. See TLS Transverse Conversion Loss. See TCL triboelectric effect 105 triboelectric series 105 TSN, Time Sensitive Networking XX, 15, 276 TVS, Transient Voltage Suppression 112 TV, television 23, 25, 27, 58 - digital 24, 25

U

UDP, User Datagram Protocol 286 Ultra Short Range Radar. See USRR ultrasonic. See sonar UL, UpLink 33, 217, 234 unbalance attenuation 131 under-voltage 170 unicast 31, 289 Unified Serial Link. See USL Universal Serial Bus. See USB unprocessed data 199 Unshielded Twisted Pair. See UTP UpLink. See UL upper tester 368 USB-C 346 USB, Universal Serial Bus 29, 122, 142, 146, 152 User Datagram Protocol. See UDP user data rates 252 USGMII 256 USL, Unified Serial Link 330 USRR, Ultra Short Range Radar 51 USXGMII 256 UTP, Unshielded Twisted Pair 16, 101, 119, 123, 138, 139

V

varistors 113 V-by-One 344, 347 VCD, Video CD 308 VCSEL, Vertical Cavity Surface-Emitting Laser 265 VDC-M, VESA Display Compression-Mobile 311 Vector Network Analyzer. See VNA vertical blanking 27 Vertical Cavity Surface-Emitting Laser. See VCSEL VESA XVIII, 341 VESA Display Compression-Mobile. See VDC-M VGA, Video Graphics Array 25, 334, 336, 341 VHDL 369 VHS, Video Home System XVII, 304 vibration 76 Video CD. See VCD video compression. *See* compression video conferencing **30** Video Graphics Array. *See* VGA Video Home System. *See* VHS Virtual Local Area Network. *See* VLAN visibility **33** visible light spectrum **35** visually lossless compression **305, 311** VLAN ID **276** VLAN, Virtual Local Area Network **276** V-model **357** VNA, Vector Network Analyzer **373** voltage regulators **167**

W

Wake-On LAN. See wake-up, WOL wake-up 152, 192, 275 - forwarding 276 - power switched 192 - signal 273 - wake-up line 193 - WOL 193 waterfall model 356 wavelengths 48, 157 white box testing 366 wire gauge 140 wiring harness 70, 78 WOL, Wake-On LAN 193, 275

Х

XAUI 256 XFI 256 XGMII 256 XPE. *See* PE-X

Y

YCbCr **303** YCoCg **303** YPbPr **303** YUV **200, 303**